| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|----------|----------------|---|---|---------------------|---------|------------------|
| L2 | 1 | (IC or (integrated adj1 circuit) or PLD or ASIC or PAL or FPGA) and (serializer with deserializer) with (transceiver) with (FIFO or (elastic adj1 buffer)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/16 10:44 |
| L3 | 24 | (IC or (integrated adj1 circuit) or PLD or ASIC or PAL or FPGA) with (transceiver) with (FIFO or (elastic adj1 buffer)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/16 10:47 |
| L5 | 31 | (IC or (integrated adj1 circuit)) with loss with (synchroniz\$5 or sync) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/16 11:10 |
| L6 | 4 6 | (IC or (integrated adj1 circuit) or PLD or ASIC or PAL or FPGA) with loss with (synchroniz\$5 or sync) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/16 11:12 |
| L7 | 16 | (IC or (integrated adj1 circuit) or PLD or ASIC or PAL or FPGA) with loss with (synchroniz\$5 or sync) with detect\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/16 12:07 |
| L12 | 21 | (IC or (integrated adj1 circuit) or PLD or ASIC or PAL or FPGA or chip) with loss with (synchroniz\$5 or sync) with (detect\$3 or monitor\$3 or determin\$5 or test\$3 or check\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/16 12:08 |
| L14 | 377 | (IC or (integrated adj1 circuit)) with (transceiver or receiver or transmitter or modem or driver) with encoder | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/16 13:22 |
| L15 | 26 | (IC or (integrated adj1 circuit)) with (transceiver) with encoder | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/16 13:23 |
| L16 | 0 | (IC or (integrated adj1 circuit)) with (transceiver or receiver or transmitter or modem or driver) with encoder with cell with momery | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/16 13:23 |
| L17 | 0 | (IC or (integrated adj1 circuit)) with (transceiver or receiver or transmitter or modem or driver) with encoder and cell with momery | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/16 13:23 |

| L18 | 0 | (IC or (integrated adj1 circuit)) with encoder and cell with momery and (transceiver or receiver or transmitter or modem or driver) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/16 13:24 |
|-----|------|---|---|----|----|------------------|
| L19 | 1762 | (IC or (integrated adj1 circuit)) with encoder and (transceiver or receiver or transmitter or modem or driver) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/16 14:18 |
| L20 | 2 | "6519753".pn. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/16 14:45 |
| L21 | 2 | "6434735".pn. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/16 14:19 |
| L22 | 2 | "6588006".pn. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/16 14:20 |
| L23 | 2 | "6539508".pn. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/16 14:21 |
| L24 | 2 | "6587995".pn. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/16 14:21 |
| L25 | 2 | "6242945".pn. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/16 14:22 |
| L26 | 2 | "6460172".pn. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/16 14:22 |
| L27 | 2 | "6353331".pn. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/16 14:22 |
| L28 | 2 | "6507942".pn. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/16 14:23 |

| L29 | 2 | "6518787".pn. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/16 14:23 |
|-----|------|---------------|---|----|----|------------------|
| L30 | 3 | "6611951".pn. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/16 14:23 |
| L31 | 2 | "6522167".pn. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/16 14:24 |
| L32 | 2 | "6532572".pn. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/16 14:24 |
| L33 | 2 | "6541991".pn. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/16 14:25 |
| L34 | 4 | "6601227".pn. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/16 14:25 |
| L35 | 9 | 09/858,732 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/16 14:44 |
| L36 | 9 | 09/861,112 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/16 14:44 |
| L37 | . 11 | 09/917,304 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/16 14:45 |
| L38 | 2 | "5907566".pn. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/16 14:46 |
| L39 | 2 | "6150837".pn. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/16 14:46 |

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|-----------|------|--|---|----|----|------------------|
| 51 | 0 | 10/082,490 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 07:18 |
| S2 | 141 | ((Suresh with Menon) or (Atul with Ghia) or (Warren with Cory) or (Paul with Sasaki) or (Philip with Freidin) or (Santiago with Asuncion) or (Philip with Costello) or (Vasisht with Vadi) or (Adebabay with Bekele) or (Hare with Verma)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 07:43 |
| S3 | 63 | ((Suresh with Menon) or (Atul with Ghia) or (Warren with Cory) or (Paul with Sasaki) or (Philip with Freidin) or (Santiago with Asuncion) or (Philip with Costello) or (Vasisht with Vadi) or (Adebabay with Bekele) or (Hare with Verma)) and (IC or (integrated adj1 circuit)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 07:44 |
| 54 | 7 | ((Suresh with Menon) or (Atul with Ghia) or (Warren with Cory) or (Paul with Sasaki) or (Philip with Freidin) or (Santiago with Asuncion) or (Philip with Costello) or (Vasisht with Vadi) or (Adebabay with Bekele) or (Hare with Verma)) and (IC or (integrated adj1 circuit)) and configuration adj1 memory adj1 cell and (transceiver or receiver or transmitter or modem) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 13:29 |
| S5 | 21 | ((Suresh with Menon) or (Atul with Ghia) or (Warren with Cory) or (Paul with Sasaki) or (Philip with Freidin) or (Santiago with Asuncion) or (Philip with Costello) or (Vasisht with Vadi) or (Adebabay with Bekele) or (Hare with Verma)) and (IC or (integrated adj1 circuit)) and configuration adj1 memory adj1 cell | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 13:30 |
| S6 | 33 | ((Suresh with Menon) or (Atul with Ghia) or (Warren with Cory) or (Paul with Sasaki) or (Philip with Freidin) or (Santiago with Asuncion) or (Philip with Costello) or (Vasisht with Vadi) or (Adebabay with Bekele) or (Hare with Verma)) and (IC or (integrated adj1 circuit)) and memory adj1 cell | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 13:30 |
| S7 | 1306 | 375/219.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 13:30 |
| S8 | 3 | ((Suresh with Menon) or (Atul with Ghia) or (Warren with Cory) or (Paul with Sasaki) or (Philip with Freidin) or (Santiago with Asuncion) or (Philip with Costello) or (Vasisht with Vadi) or (Adebabay with Bekele) or (Hare with Verma)) and (IC or (integrated adj1 circuit)) and configuration adj1 memory adj1 cell and transceiver | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 14:17 |
| S9 | 1 | (IC or (integrated adj1 circuit)) and configuration adj1 memory adj1 cell with transceiver with select\$4 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 14:18 |
| S10 | 3 | (IC or (integrated adj1 circuit)) and configuration adj1 memory adj1 cell with transceiver | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 14:22 |
| S11 | 1 | "6661794".PN. | USPAT; USOCR | OR | ON | 2005/09/15 14:19 |
| S12 | 1 | "6633865".PN. | USPAT; USOCR | OR | ON | 2005/09/15 14:21 |

| S13 | 25 | (IC or (integrated adj1 circuit)) and configuration adj1 memory adj1 cell with (transceiver or receiver or transmitter or modem or driver) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 16:49 |
|-----|-----|---|---|----|----|------------------|
| 518 | 9 | (IC or (integrated adj1 circuit)) and configuration adj1 memory adj1 cell and (transceiver or receiver or transmitter or modem or driver or sender) and cyclic adj1 redundancy | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 14:45 |
| S20 | 4 | (IC or (integrated adj1 circuit)) and configuration adj1 memory adj1 cell same (transceiver or receiver or transmitter or modem or driver or sender) and cyclic adj1 redundancy | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 14:34 |
| S23 | 7 | (IC or (integrated adj1 circuit)) and configuration adj1 memory adj1 cell and cyclic adj1 redundancy with (verif\$7 or generat\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 14:51 |
| S24 | 6 | "6664808" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 14:49 |
| S25 | 3 | "6664808" and CRC | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 14:48 |
| S26 | 3 | "6664808" and CRC with (generat\$3 or verif\$7) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 14:49 |
| S27 | 7 | (IC or (integrated adj1 circuit) or PLD or ASIC or PAL or FPGA) and configuration adj1 memory adj1 cell and cyclic adj1 redundancy with (verif\$7 or generat\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 14:53 |
| S28 | 940 | (IC or (integrated adj1 circuit) or PLD or ASIC or PAL or FPGA) and cyclic adj1 redundancy with (verif\$7 or generat\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 14:56 |
| S29 | 30 | (IC or (integrated adj1 circuit) or PLD or ASIC or PAL or FPGA) and cyclic adj1 redundancy with (verif\$7 with generat\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 15:02 |
| S30 | 112 | (IC or (integrated adj1 circuit) or PLD or ASIC or PAL or FPGA) and (cyclic adj1 redundancy or CRC) with (verif\$7 with generat\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 14:53 |

| 531 | 102 | (IC or (integrated adj1 circuit) or PLD or ASIC or PAL or FPGA) | US-PGPUB; | OR | ON | 2005/09/15 14:54 |
|-----|------|---|---|----|----|------------------|
| 331 | 102 | and (cyclic adj1 redundancy adj1 cod\$3 or CRC) with (verif\$7 with generat\$3) | USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | | | 2003/03/13 11.34 |
| S32 | 101 | (IC or (integrated adj1 circuit) or PLD or ASIC or PAL or FPGA) and (cyclic adj1 redundancy adj1 cod\$3 or CRC) with (verif\$7 with generat\$3) and (transceiver or receiv\$3 or send\$3 or transmit\$4 or driv\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 14:54 |
| 534 | 97 | (IC or (integrated adj1 circuit) or PLD or ASIC or PAL or FPGA) and cyclic adj1 redundancy with (verif\$7 or generat\$3) and memory adj1 cell | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 14:56 |
| S39 | 1025 | (IC or (integrated adj1 circuit) or PLD or ASIC or PAL or FPGA) and (serializer and deserializer) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 15:05 |
| 540 | 869 | (IC or (integrated adj1 circuit) or PLD or ASIC or PAL or FPGA) and (serializer with deserializer) and (transceiver or receiv\$3 or transmi\$5 or modem or driv\$3 or send\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 15:06 |
| 541 | 867 | (IC or (integrated adj1 circuit) or PLD or ASIC or PAL or FPGA) and (serializer with deserializer) and (transceiver or receiv\$3 or transmi\$5 or driv\$3 or send\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 16:48 |
| S42 | 439 | (IC or (integrated adj1 circuit) or PLD or ASIC or PAL or FPGA) and (serializer with deserializer) with (transceiver or receiv\$3 or transmi\$5 or driv\$3 or send\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 15:06 |
| 543 | 2 | (IC or (integrated adj1 circuit) or PLD or ASIC or PAL or FPGA) and (serializer with deserializer) with (transceiver or receiv\$3 or transmi\$5 or driv\$3 or send\$3) and cell adj1 memory | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 15:13 |
| 544 | 120 | (IC or (integrated adj1 circuit) or PLD or ASIC or PAL or FPGA) and (serializer with deserializer) with (transceiver or receiv\$3 or transmi\$5 or driv\$3 or send\$3) and CRC | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 15:21 |
| S46 | 19 | (IC or (integrated adj1 circuit) or PLD or ASIC or PAL or FPGA) and (serializer and deserializer) with (transceiver or receiv\$3 or transmi\$5 or driv\$3 or send\$3) and cell adj1 memory | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 15:23 |
| 547 | 44 | (IC or (integrated adj1 circuit) or PLD or ASIC or PAL or FPGA) and (serializer with deserializer) with (transceiver) and CRC | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 15:21 |

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|-----|------|---|---|----|----|------------------|
| S48 | 118 | (IC or (integrated adj1 circuit) or PLD or ASIC or PAL or FPGA) and (serializer with deserializer) with (transceiver) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/16 10:42 |
| 549 | . 23 | (IC or (integrated adj1 circuit) or PLD or ASIC or PAL or FPGA) and (serializer and deserializer) and cell adj1 memory | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 15:25 |
| S51 | 25 | (IC or (integrated adj1 circuit) or PLD or ASIC or PAL or FPGA) and ((serializer and deserializer) or serdes) and cell adj1 memory | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 15:29 |
| S53 | 1 | (IC or (integrated adj1 circuit) or PLD or ASIC or PAL or FPGA) with cell adj1 memory and ((serializer and deserializer) or serdes) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 15:26 |
| S54 | 40 | (IC or (integrated adj1 circuit) or PLD or ASIC or PAL or FPGA) and ((serializer or deserializer) or serdes) and cell adj1 memory | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 15:57 |
| S55 | 38 | (IC or (integrated adj1 circuit) or PLD or ASIC or PAL or FPGA) and ((serializer or deserializer)) and cell adj1 memory | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 16:33 |
| S56 | 13 | 09/968,446 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 16:18 |
| S59 | 2 | "5361373".pn. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 16:19 |
| S60 | 2 | "5361373" and (serializer or deserializer) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 17:19 |
| S61 | 1 | "5537601" and (serializer or deserializer) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 17:20 |
| S62 | 1 | "5652904" and (serializer or deserializer) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 16:23 |

| S63 | 2 | "5671355" and (serializer or deserializer) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 16:24 |
|-----|---|--|---|----|----|------------------|
| S64 | 1 | "5752035" and (serializer or deserializer) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 16:24 |
| S65 | | "5970254" and (serializer or deserializer) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 16:25 |
| S66 | 4 | "6020755" and (serializer or deserializer) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 16:27 |
| S67 | 0 | "6092091" and (serializer or deserializer) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 16:27 |
| S68 | 2 | "6096091" and (serializer or deserializer) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 16:27 |
| S69 | 1 | "6279045" and (serializer or deserializer) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 16:27 |
| S70 | 1 | "6282627" and (serializer or deserializer) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 16:28 |
| S71 | 1 | "6343207" and (serializer or deserializer) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 16:28 |
| S72 | 0 | "5574093" and (serializer or deserializer) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 16:28 |
| S73 | 0 | "5574930" and (serializer or deserializer) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 16:28 |

| S74 | 1 | "5600845" and (serializer or deserializer) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR C | ON | 2005/09/15 16:29 |
|-----|---|---|---|------|----|------------------|
| S75 | 0 | "5742179" and (serializer or de\$2serializer) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR C | ON | 2005/09/15 16:30 |
| S76 | 0 | "5581745" and (serializer or de\$2serializer) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR C | ON | 2005/09/15 16:30 |
| S77 | 0 | "5740404" and (serializer or de\$2serializer) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR C | ON | 2005/09/15 16:30 |
| S78 | 0 | "5740404" and (serializer or deserializer) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR C | ON | 2005/09/15 16:30 |
| S79 | 0 | "5457410" and (serializer or de\$2serializer) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR (| ON | 2005/09/15 16:30 |
| 580 | 2 | "5914616" and (serializer or de\$2serializer) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR 0 | ON | 2005/09/15 16:31 |
| 581 | 0 | "6467009" and (serializer or de\$2serializer) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR C | ON | 2005/09/15 17:20 |
| S82 | 0 | "5552722" and (serializer or de\$2serializer) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 16:31 |
| S83 | 0 | "5504738" and (serializer or de\$2serializer) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR (| ON | 2005/09/15 16:31 |
| 584 | 3 | "5311114" and (serializer or de\$2serializer) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR (| ON | 2005/09/15 16:32 |

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|------|-----|--|---|----|----|------------------|
| S85 | 1 | "5311114" and (de\$2serializer) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 16:32 |
| S86 | 194 | (IC or (integrated adj1 circuit) or PLD or ASIC or PAL or FPGA) with ((deserializer)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 16:43 |
| S87 | 3 | (IC or (integrated adj1 circuit) or PLD or ASIC or PAL or FPGA) with ((deserializer)) and cell adj2 memory | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 16:36 |
| \$88 | 13 | (IC or (integrated adj1 circuit) or PLD or ASIC or PAL or FPGA) with ((deserializer)) and cell with memory | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 16:36 |
| 589 | 4 | (IC or (integrated adj1 circuit) or PLD or ASIC or PAL or FPGA) and ((deserializer or serializer)) with comma with detect\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 16:44 |
| 592 | 195 | (IC or (integrated adj1 circuit)) and (transceiver or receiver or transmitter or modem or driver) and loss with synchroniz\$5 with detect\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/16 13:21 |
| S95 | 69 | (IC or (integrated adj1 circuit)) and (transceiver or receiver or transmitter or modem or driver) same loss with synchroniz\$5 with detect\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 17:32 |
| S96 | 46 | (IC or (integrated adj1 circuit)) and (transceiver or receiver or transmitter or modem or driver) with loss with synchroniz\$5 with detect\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 16:55 |
| S97 | 4 | (IC or (integrated adj1 circuit)) with loss with synchroniz\$5 with detect\$3 and (transceiver or receiver or transmitter or modem or driver) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 16:53 |
| S98 | 0 | (IC or (integrated adj1 circuit)) same (transceiver or receiver or transmitter or modem or driver) with loss with synchroniz\$5 with detect\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 16:55 |
| S99 | 3 | (IC or (integrated adj1 circuit)) with loss adj1 synchroniz\$5 with detect\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 17:13 |

| S10 0 | 5 | (IC or (integrated adj1 circuit)) with loss adj2 (synchroniz\$5 or sync) with detect\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 17:15 |
|----------|----|---|---|----|----|------------------|
| S10 1 | 12 | (IC or (integrated adj1 circuit)) with loss adj2 (synchroniz\$5 or sync) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/16 11:07 |
| S10 2 | 0 | "5361373" and (loss adj2 synchroniz\$5 with detect\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 17:19 |
| S10 3 | 0 | "5537601" and (loss adj2 synchroniz\$5 with detect\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 17:20 |
| S10 4 | 0 | "6467009" and (loss adj2 synchroniz\$5 with detect\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 17:20 |
| S10 5 | 3 | (IC or (integrated adj1 circuit)) and memory adj1 cell and loss with synchroniz\$5 with detect\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 17:23 |
| S10 8 | 9 | configur\$5 adj1 transceiver and loss with synchroniz\$5 with detect\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 17:25 |
| S10 9 | 2 | high adj1 speed adj1 configur\$5 adj1 transceiver adj1 architecture | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 17:29 |
| S11 0 | 3 | configur\$5 adj1 transceiver adj1 architecture | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 17:29 |
| S11 1 | 21 | (IC or (integrated adj1 circuit)) and (transceiver or receiver or transmitter or modem or driver) with program\$5 with fabric | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 17:34 |
| S11 2 | 11 | (IC or (integrated adj1 circuit)) and (transceiver or receiver or transmitter or modem or driver) with programmable with fabric | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 17:35 |

| S11 3 | 16 | (IC or (integrated adj1 circuit)) and (transceiver or receiver or transmitter or modem or driver or sender) same programmable with fabric | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 18:09 |
|----------|------|---|---|----|----|------------------|
| S11 4 | 2 | (IC or (integrated adj1 circuit) or PLD or ASIC or PAL or FPGA) and (transceiver or receiver or transmitter or modem or driver or sender) and programmable with fabric and processor adj2 core and signal adj2 path | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 18:12 |
| S11 5 | 3 | (IC or (integrated adj1 circuit) or PLD or ASIC or PAL or FPGA) and (transceiver or "I/O") and programmable adj2 fabric and processor adj2 core and signal adj2 path | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/16 11:06 |
| S11 6 | 1249 | 326/39.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 18:24 |
| S11 7 | 7 | 326/39.ccls. and programmable adj2 fabric | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 18:26 |
| 511 8 | 15 | "326"/.ccls. and programmable adj2 fabric | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 18:30 |
| S11 9 | 4 | "6570492" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/15 18:30 |